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Dr. Kaibalya Prasad Panda

Dr. Kaibalya Prasad Panda obtained the Ph.D. degree in the Electrical Engineering from National Institute of Technology Meghalaya, Shillong, India, in 2021. From July 2013 to June 2017, he was an Assistant Professor with the Department of EEE, C. V. Raman College of Engineering, Bhubaneswar, India. After his Ph.D., he served as Ad-hoc faculty in NIT Andhra Pradesh from Oct 2021 to June 2022. Currently, he is working as Assistant Professor with the Department of EE, School of Technology, Pandit Deendayal Energy University (PDEU), Gandhinagar, Gujarat, India since Sept 2022. His research interests include reduced switch multilevel inverter design, new power converters, power quality control and photovoltaic system applications.

He was a visiting scholar with the University of Warwick, U.K., during January to March 2019, funded through the Joint UK-India Clean Energy (JUICE) two months exchange program scheme. He was recipient of Prof. Subhasis Nandi Student Travel Awards for Power Electronics, Drives and Energy Systems (PEDES 2018) conference and IAS Travel grant for Power Electronics, Smart Grid and Renewable Energy (PESGRE 2020) conference. He has received the Institute best research award during his Ph.D. and best paper award for one of his papers presented in ICEPE 2020 Conference. He has authored/co-authored over 10 IEEE Transactions and reputed journals/conferences and also filed two patents. Consecutively in 2021, 2022 & 2023, he has been listed in World's Top 2% of Researchers under the Energy category as per the Stanford University and Elsevier list. He is a regular Reviewer of several IEEE and IET Journals.